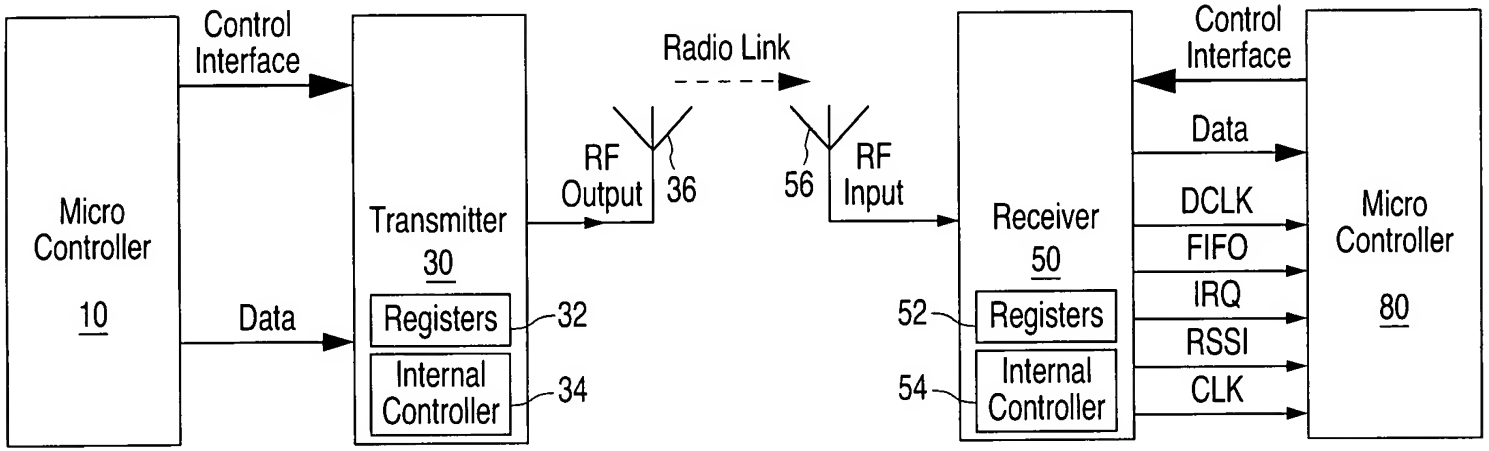
Micro Controller 10
Control Interface
Data
Transmitter 30
Registers
32
Internal Controller
34
RF Output
36
Radio Link
56
RF Input
Receiver 50
52
Registers
54
Internal Controller
Control Interface
Data
DCLK
FIFO
IRQ
RSSI
CLK
Micro Controller 80

FIG. 1

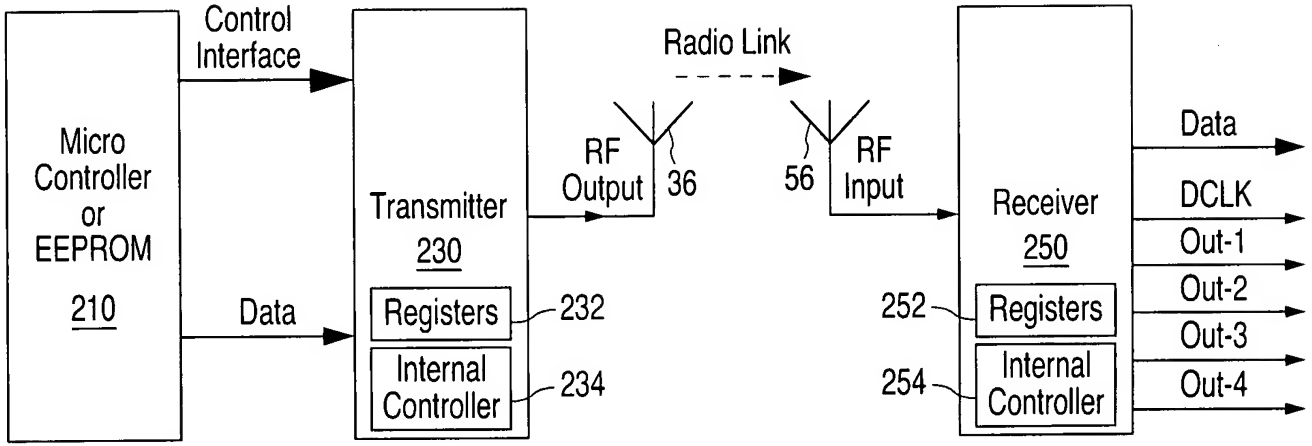
Micro Controller or EEPROM 210
Control Interface
Data
Transmitter 230
Registers
232
Internal Controller
234
RF Output
36
Radio Link
56
RF Input
Receiver 250
252
Registers
254
Internal Controller
Data
DCLK
Out-1
Out-2
Out-3
Out-4

FIG. 6

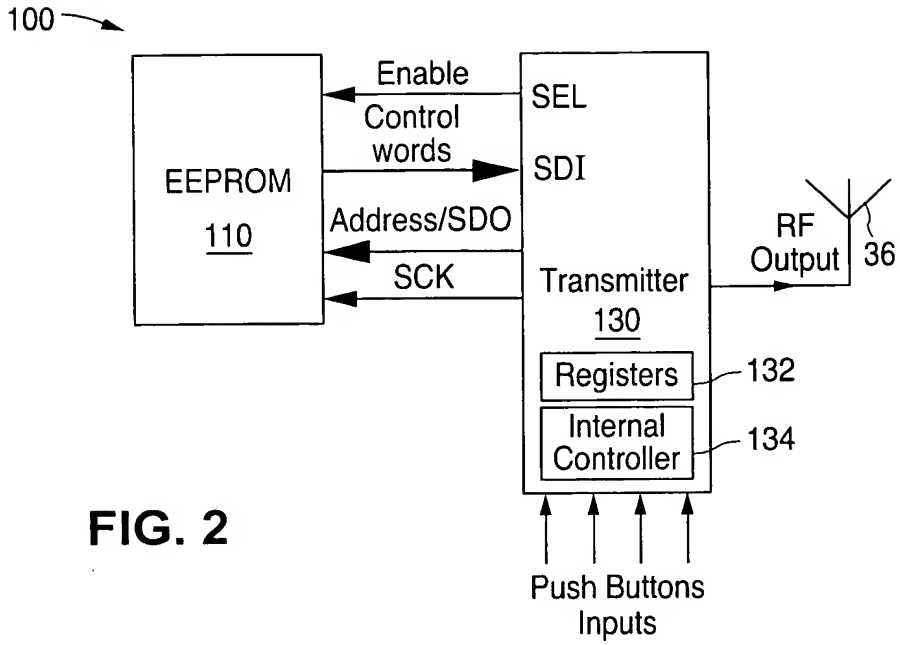
100
EEPROM
110
Enable
Control
words
Address/SDO
SCK
SEL
SDI
Transmitter
130
Registers
132
Internal
Controller
134
RF
Output
36
Push Buttons
Inputs

FIG. 2

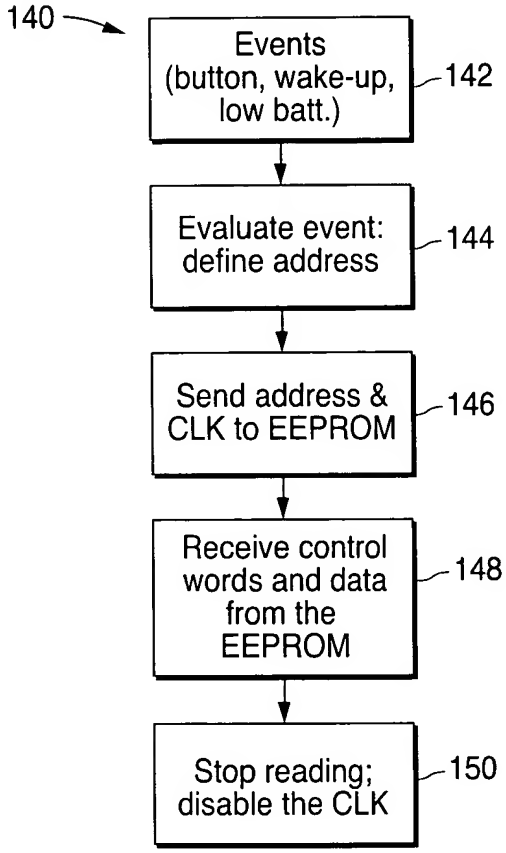
140
Events
(button, wake-up,
low batt.)
142
Evaluate event:
define address
144
Send address &
CLK to EEPROM
146
Receive control
words and data
from the
EEPROM
148
Stop reading;
disable the CLK
150

FIG. 3

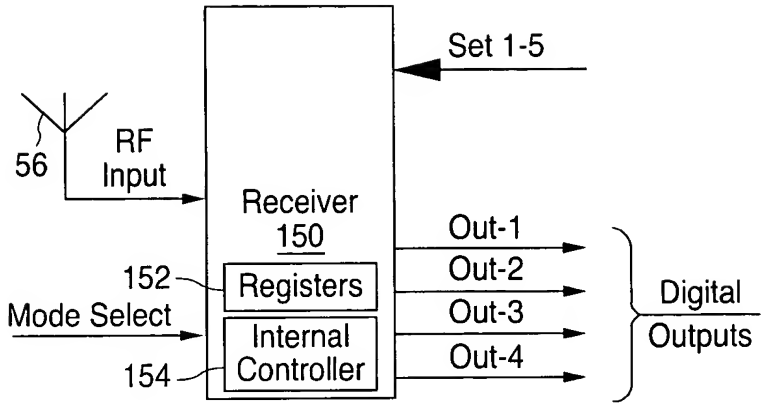
Set 1-5
56
RF
Input
Receiver
150
Out-1
Out-2
Out-3
Out-4
Digital
Outputs
152
Registers
Mode Select
Internal
Controller
154

FIG. 4

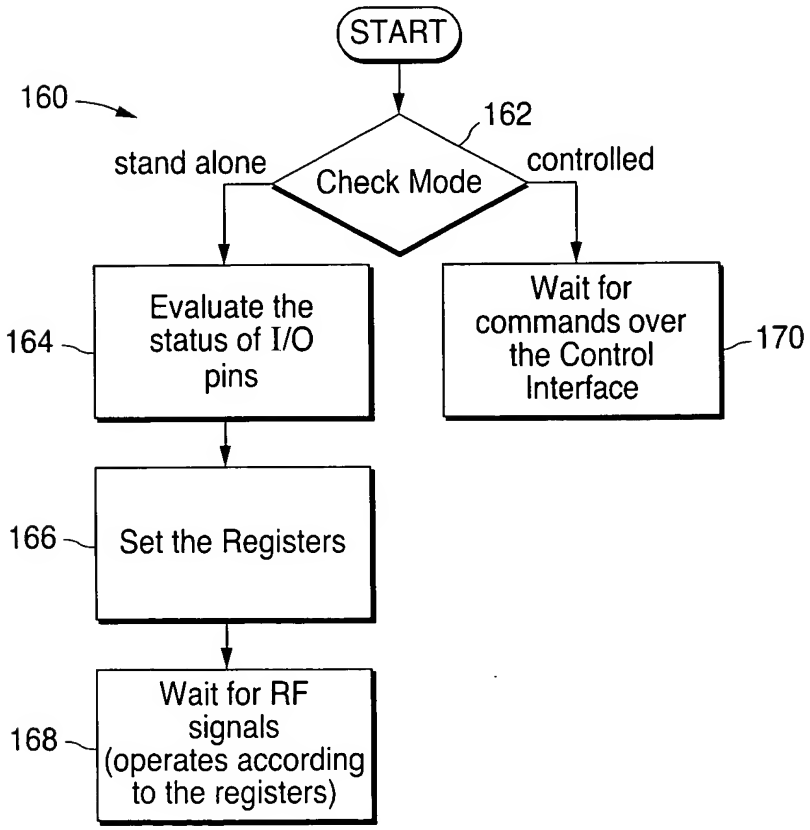
START
160
162
stand alone
Check Mode
controlled
Evaluate the
status of I/O
pins
164
Wait for
commands over
the Control
Interface
170
Set the Registers
166
Wait for RF
signals
(operates according
to the registers)
168

FIG. 5

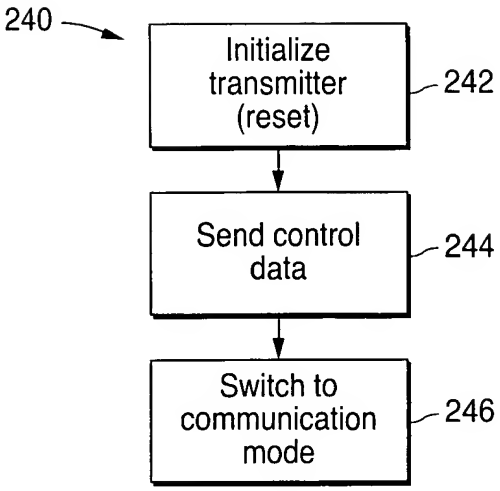
240
Initialize transmitter (reset)
242
Send control data
244
Switch to communication mode
246

FIG. 7

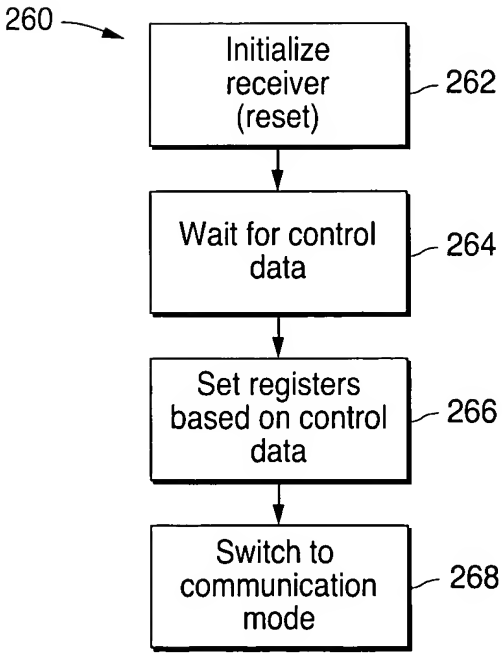
260
Initialize receiver (reset)
262
Wait for control data
264
Set registers based on control data
266
Switch to communication mode
268

FIG. 8